

Notice of References Cited		Application No. 08/720,693	Applicant(s) Kao et al
		Examiner George Fourson	Group Art Unit 2823
U.S. PATENT DOCUMENTS			

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
A					
B					
C					
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FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
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S						
T						

NON-PATENT DOCUMENTS

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U	Wolf, S. et al, "Silicon Processing For the VLSI Era: Vol.1, Process Technology", Lattice Press, pp.323-324	1986
V		
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